



Academic Personnel Short Profile / Short CV

University:	University of Patras – Hellas
Surname:	Sklavos
Name:	Nicolas
Rank:	Associate Professor
Faculty:	Polytechnic School
Department:	Computer Engineering and Informatics
Scientific Domain: *	Cryptographic Engineering, Hardware Security, Cyber Security, Digital Systems Design, Embedded Systems

** Field of Specialization*

Academic qualifications (list by highest qualification)				
Qualification	Year	Awarding Institution	Department	Thesis title
PhD Degree	2004	University of Patras	Electrical and Computer Engineering	«VLSI Designs of Wireless Communications Security Systems»
Diploma and M.Sc. of Engineering	2000	University of Patras	Electrical and Computer Engineering	«Asynchronous VLSI Implementation of IDEA Algorithm»

Employment history – List by the three (3) most recent				
Period of employment		Employer	Location	Position
From	To			
06/2015	today	University of Patras – Computer Engineering and Informatics Department	Patra, Hellas	Associate Professor
09/2015	today	Open University of Cyprus (OUC)	Nicosia, Cyprus	Adjunct Faculty
10/2006	12/2018	Computer Technology Institute and Press “Diophantus”	Patra, Hellas	Senior Researcher

Key <u>refereed</u> journal papers, monographs, books, conference publications etc. List the five (5) more recent and other five (5) selected –(max total 10)						
Ref. Number	Year	Title	Other authors	Journal and Publisher / Conference	Vol.	Pages
1	2020	Cryptographic Technologies and Protocol Standards for Internet of Things	Sherali Zeadally, Ashok Kumar Das	Internet of Things: Engineering Cyber Physical Human Systems, Elsevier	-	-
2	2020	Cryptographic System for Data Applications, in the Context of the Internet of Things	P. Panagiotou, E. Darra, I. D. Zaharakis	Microprocessors and Microsystems, Elsevier	22	1-12
3	2020	An Efficient Implementation of A Delay-Based PUF Construction	A. Antoniadis, E. Bilge Kavun	Trustworthy Manufacturing and Utilization of Secure Devices Workshop, Design, Automation and Test in Europe	-	-

				Conference, (DATE'20)		
4	2019	On the Hardware Implementation Performance, of Face Recognition Techniques, for Digital Forensics	M. Pantopoulou, I. Ognjanovic	7th International Symposium on Security in Computing and Communications (SSCC'19), Springer	-	-
5	2018	Architecture Design of an Area Efficient High Speed Crypto Processor for 4G LTE	A. N. Bikos	IEEE Transactions on Dependable and Secure Computing	15	729-741
6	2020	Lightweight Efficient Simeck32/64 Crypto-Core Designs and Implementations, for IoT Security	S. Limnaios, O. Koufopavlou	27th IFIP/IEEE International Conference On Very Large Scale Integration (VLSI-Soc'19)	-	-
7	2019	Post-Quantum Signature Schemes for Efficient Hardware Implementation	D. N. Moldovyan, A. A. Moldovyan	10th IFIP International Conference on New Technologies, Mobility & Security (NTMS'19)	-	-
8	2019	Blockchain Based Security & Privacy in Smart Cities	S. Theodorou	Chapter in the Book: Smart Cities Cybersecurity and Privacy, Elsevier		
9	2018	Container-Based Honeypot Deployment for the Analysis of Malicious Activity	A. Kyriakou	Global Information Infrastructure and Networking Symposium (GIIS'18)	-	-
10	2017	Hardware Security and Trust	R. Chaves, G. Di Natale, F. Regazzoni	Springer	-	-

Exhibitions (where applicable). List the five (5) more recent and other five (5) selected. (max total 10)					
Ref. Number	Date	Topic	International / Local	Location*	Role in Exhibition
1	04/2015	Research Group: Cybersecurity of Computers and Networks	"Patras IQ" - International	Patra - Hellas	Head for the Research Group's Exhibition/Invited Speaker
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** Specify venue, geographic location etc*

Research Projects. List the five (5) more recent and other five (5) selected (max total 10)				
Ref. Number	Date	Title	Funded by	Project Role*
1	03/2017 – 03/2021	MULTI-modal Imaging of FOREnsic SciEnce Evidence - Tools for Forensic Science	European Commission	Member of Management Committee, Senior Researcher
2	06/2016 – 05/2019	HORIZON 2020: Exploiting Ubiquitous Computing, Mobile Computing and the Internet of Things to promote Science Education	European Commission	Senior Researcher
3	12/2014 – 12/2018	Cryptanalysis of Ubiquitous Computing Systems (CRYPTACUS)	European Commission	Member (Substitute) of Management Committee, Senior Researcher

4	04/2014 – 04/2018	Cryptography for Secure Digital Interaction (CRYPTOACTION)	European Commission	Member (Substitute) of Management Committee, Senior Researcher
5	12/2012 – 12/2016	Trustworthy Manufacturing and Utilization of Secure Devices (TRUDEVICE)	European Commission	Member of Management Committee, Working Group 5, Co-Chair, Dissemination Manager, Senior Researcher
6	2012-2016	Timing Analysis on Code-Level (TACLe)	European Commission	Member of Management Committee, Dissemination Manager, Senior Researcher
7	2013	LLP Erasmus Intensive Programmes (IP), “Mobile Sensing & Technologies”	National Institute of Scholarships - Hellas	Scientific Coordinator of the Department - Institute
8	2011-2013	Students Industry Placement	European Commission & National Resources	Scientific Coordinator of the Department
9	10/2006-02/2009	FP6-2005-IST-5, “Phosphorus – Lamda User Controlled Infrastructure for European Research”	European Commission	Senior Researcher – Working Group Member
10	2005-2006	Post-Doctoral Research Project	National Institute of Scholarships - Hellas	Senior Researcher

**Project Role: i.e. Scientific/Project Coordinator, Research Team Member, Researcher, Assistant Researcher, other*

Consulting Services and/or Participation in Councils / Boards/ Editorial Committees. List the five (5) more recent				
Ref. Number	Period	Organization	Title of Position or Service	Key Activities
1	2015 - today	European Commission	Evaluator\Reviewer for European Commission R&D Projects: HORIZON 2020	Evaluator\Reviewer for Proposals and Ongoing, R&D Projects
2	2018 - today	European Commission	Evaluator\Reviewer for COST Actions: European Cooperation in Science and Technology	Evaluator\Reviewer for Proposals of COST Actions
3	2012 - today	European Commission and National Agents	Evaluator\Reviewer for International\National Projects' Calls	Evaluator\Reviewer for Proposals and Ongoing, R&D Projects for International\National Projects Calls: Croatia, Hellas, Israel, Italy, Latvia
4	2020 - today	University of Patras	Director of the Division: Hardware and Computers Architecture	Academic, Scientific, Research, Educational, Management Responsibilities – Computer Engineering and Informatics Department
5	2015 - today	University of Patras	Member of the Management Committee of M.Sc. Program "Integrated Systems of Hardware & Software"	Academic, Scientific, Research, Educational, Management Responsibilities – Computer Engineering and Informatics Department

Awards / International Recognition (where applicable). List the five (5) more recent and other five (5) selected. (max total 10)			
Ref. Number	Date	Title	Awarded by:
1	2002 - today	More than 2900 citations, for publications of Nicolas Sklavos	International Scientific Literature, Source: Google Scholar
2	07/2017	Best Diploma Thesis Award, Student: Eleni Isa, Supervisor: Nicolas Sklavos, "On the Detection of Hardware Trojans, in Hardware Security"	Design Test Verification and EDA (DTVEDA'07) Workshop, 2017
3	2007-2016	IEEE Young Professionals (Greece) – Council's Chair	IEEE
4	12/2012	IEEE Senior Member	IEEE (New Brunswick, NJ, USA, at Admission and Advancement (A&A) Review Panel Meeting)
5	01/2012	Top Associate Editor in 2011	Computers & Electrical Engineering, Elsevier Science Press
6	01/2011	Top Associate Editor in 2010	Computers & Electrical Engineering, Elsevier Science Press
7	2005-2006	Scholarship for Post-Doctoral Research	National Institute of Scholarships - Hellas
8	12/2003	Best PhD Thesis Award, for Nicolas Sklavos	IFIP VLSI SOC Conference, Germany, 2013
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Other Achievements. List the five (5) more recent and other five (5) selected. (max total 10)			
Ref. Number	Date	Title	Key Activities:
1	2011-2014	Editor In Chief, Information Security Journal: A Global Perspective, The Official Journal of (ISC)2, A Taylor & Francis Group Publication	Editor In Chief
2	2019 - today	Associate Editor, IEEE VLSI Circuits and Systems Letter, IEEE	Associate Editor
3	2009 - today	Associate Editor, Information Security Journal: A Global Perspective, The Official Journal of (ISC)2, A Taylor & Francis Group Publication	Associate Editor
4	2018 - today	Handling Editor, Microprocessors and Microsystems: Embedded Hardware Design (MICPRO), Elsevier Science Press	Handling Editor
5	2015 - today	Associate Editor, Cryptography, MDPI Publisher	Associate Editor
6	2020 - today	Associate Editor, Internet of Things - IoT, MDPI Publisher	Associate Editor
7	2011 - today	Associate Editor, IEEE Latin America Transactions, IEEE	Associate Editor
8	2018 - today	Advisory Board Member, Embedded Systems, Cambridge Scholars Publishing	Advisory Board Member
9	2012 - 2018	Journal of Electrical and Computer Engineering, Hindawi	Associate Editor
10	2004 - today	Participation to the committees of more than 350 international scientific conferences organised by IEEE, ACM, IFIP etc	Serving Roles: General Chair, Program Chair, Publication Chair, Publicity Chair, Program Committee Member, and other roles